Milestones Report

Group 1:  
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Target application: **Embedded Digital Signal Processing**

* (1.5 weeks) Arithmetic modules improvement and simulation
  + Multiplier Improvement: Full-tree Multiplier (or others + Performance\*Area evaluation)
  + Divider Improvement: Radix-4 or Repeated Multiplication (or others) + Performance\*Area evaluation
* (2-3 days) System simulation with arithmetic improvements
* (5 days)Critical path analysis and improvements
* (5 days)Study of possible architecture improvements
* (4 days)Final report & discussion